IN THE CLAIMS

Amend Claims 1, 2, 3, 4, 6, 8, 11, and 13.

Remain Claim 5, 7, 9, 10 and 12.

Cancel Claims 14 and 15.

Amend Claim 16.

Please Amend Claim 1 as follows:

1. (Currently Amended) A state-displaying device for displaying state data generated by a data-processing device, said state-displaying device comprises:

an universal asynchronous receiver/transmitter interface for receiving state data for outputting said state data in a serial mode; and

a displaying device including:

(a) a microprocessor coupled to said universal asynchronous receiver/transmitter interface for outputting a displaying signal in corresponding to said state data output by said universal asynchronous receiver/transmitter interface; and

(b) a multi-segment display module coupled to said microprocessor for displaying a symbol in corresponding to said displaying signal;

wherein said state data is generated by a BIOS program of said data-processing device and/or said state data is generated by a detecting application program of said data-processing device.

Please amend Claim 2 as follows:

2. (Currently Amended) The state-displaying device as in claim 1, wherein said multi-segment display module [at least] is composed of at least one seven- segment

display.

Please amend Claim 3 as follows:

3. (Currently Amended) The state-displaying device as in claim 1, wherein said data-processing device is [selected between] a server [and] or a personal computer.

Please amend Claim 4 as follows:

4. (Currently Amended) The state-displaying device as in claim 1, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Please amend Claim 6 as follows:

6. (Currently Amended) The state-displaying device as in claim 1, wherein said state data output by said universal asynchronous receiver/ transmitter interface [is of a] complies the specification of RS-232.

Please amend Claim 8 as follows:

8. (Currently Amended) The state-displaying device as in claim 1, wherein said state data includes an on/off bit, at least [a] one command mode bit and a plurality of displaying bits, said command mode bit is used to define a mode of displaying of said displaying bits, said microprocessor decides a mode of displaying of said multi- segment display module according to said mode of displaying of said displaying bits.

Please amend Claim 11 as follows:

11. (Currently Amended) The state-displaying device as in claim 10, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Please amend Claim 13 as follows:

13. (Currently Amended) The state-displaying device as in claim 12, wherein said symbol is [selected among] a numeral, an English letter [and] or a specific character.

Please cancel Claims 14 and 15.

14. (Cancelled)

15. (Cancelled)

Please amend Claim 16 as follows:

14 [16] The state-displaying device as in claim 1 [15], wherein said detecting application program is executed in an operating system of said data-processing device.